



SAINT LOUIS UNIVERSITY
MADRID

ECE-2206-M36: Digital Design Lab
Spring 2018

Class Days and Time: T, 17:30-19:20

Classroom: PAH-21

Prerequisite(s): Prior or concurrent enrollment in ECE-2205

Credit(s): 1

Instructor: Usama Abou-Kheder

Instructor's Email: usama.kheder@slu.edu

Instructor's Campus Phone: 91 554 58 58, ext. 266. **Mobile Phone:** 600 742 339

Office: Sciences and Engineering Office (PAH) or Physics Lab (PAH-21)

Office Hours: W, 17:30-20:30

Course Description: This lab exercises the theoretical concepts covered in ECE-2205. A circuit board containing a programmable logic device (PLD) in conjunction with PC software is used to illustrate a typical electronic design flow. Students must complete a final project in which the PLD interacts with other devices.

Course Goals and Student Learning Outcomes: At the end of the course, students will be able to:

- Design and implement basic combinational circuits (logic gates, decoders, encoders, multiplexers)
- Design and implement basic sequential circuits (registers, latches, shift registers, counters)
- Design and implement complex circuits using finite state machines
- Understand the function and behavior of a circuit from its schematic
- Understand the function and behavior of a circuit from its VHDL description
- Implement combinational and sequential circuits into CPLDs and FPGAs using the VHDL language
- Analyze the results of compilers and synthesizers
- Simulate circuits using VHDL language and CAD programs

Saint Louis University - Madrid Campus is committed to excellent and innovative educational practices. In order to maintain quality academic offerings and to conform to relevant accreditation requirements, the Campus regularly assesses its teaching, services, and programs for evidence of student learning outcomes achievement. For this purpose anonymized representative examples of student work from all courses and programs is kept on file, such as assignments, papers, exams, portfolios, and results from student surveys, focus groups, and reflective exercises. *Thus, copies of student work for this course, including written assignments, in-class exercises, and exams may be kept on file for institutional research, assessment and accreditation purposes.* If students prefer that Saint Louis University - Madrid Campus does not keep their work on file, they need to communicate their decision in writing to the professor.

Required Texts and Materials:

- ECE-2206 Digital Design Lab Manual

Reference:

- Basys 3 FPGA Board Reference Manual, available on the Internet (https://reference.digilentinc.com/media/basys3:basys3_rm.pdf)

- Synario VHDL Reference Manual, available on the Internet
(<http://www.ics.uci.edu/~jmoorkan/vhdlref/Synario%20VHDL%20Manual.pdf>)
(<https://www.scribd.com/document/338783587/Synario-VHDL-Manual-pdf>)
- The Student's Guide to VHDL- Peter J. Ashenden, Published by Morgan Kaufmann
- Digital Design Principles and Practices, 4/E - J. F. Wakerly. Published by Prentice Hall.

Attendance Policy: Attendance to all lab sessions is mandatory. If an absence is justified, the student and the instructor can appoint a day to make up the missing lab session.

Course Requirements and Grading Rationale/System: A short report will be written after each lab session. It will be sent by email to the instructor before the beginning of the next lab session. The lab report will include the following sections:

- Introduction: Devices and concepts studied in the lab session. Platform used (board, language, software).
- Description: Procedure, diagrams/schematics. It will also include the VHDL code of each circuit used in the experiment. Commented results.
- Conclusion: What has been learnt in the lab session. Did the experimental results coincide with the theory? Commentaries on the overall results. How the experiment can be improved. Difficulties encountered. Applications.
- An appendix with all the code used in the lab session

The final project will also have a report.

The final grade will be obtained from the following areas:

Lab Sessions: **30%**

Lab Reports: **30%**

Final Project: **40%**

Grading scale:

A	90-100%
A-	87-89%
B+	84-86%
B	80-83%
B-	77-79%
C+	74-76%
C	70-73%
C-	66-69%
D	60-65%
F	< 60%

E-mail: Campus and course announcements will often be handled by e-mail. Students should check their "@slu.edu" e-mail regularly.

University Statement on Academic Integrity: Academic integrity is honest, truthful and responsible conduct in all academic endeavors. The mission of Saint Louis University is "the pursuit of truth for the greater glory of God and for the service of humanity." Accordingly, all acts of falsehood demean and compromise the corporate endeavors of teaching, research, health care and community service via which SLU embodies its mission. The University strives to prepare students for lives of personal and professional integrity, and therefore regards all breaches of academic integrity as matters of serious concern.

The governing University-level Academic Integrity Policy can be accessed on the Provost's Office [website](#). Additionally, SLU-Madrid has posted its academic integrity policy online: <http://www.slu.edu/madrid/academics>. As a member of the University community, you are expected to know and abide by these policies, which detail definitions of violations, processes for reporting violations, sanctions and appeals.

The professor will review these matters during the first weeks of the term. Please direct questions about any facet of academic integrity to your faculty, the chair of the department of your academic program, or the Academic Dean of the Madrid Campus.

University Title IX Statement: Saint Louis University and its faculty are committed to supporting our students and seeking an environment that is free of bias, discrimination, and harassment. If you have encountered any form of sexual misconduct (e.g. sexual assault, sexual harassment, stalking, domestic or dating violence), we encourage you to report this to the University. If you speak with a faculty member about an incident of misconduct, that faculty member must notify SLU's Title IX deputy coordinator, Marta Maruri, whose office is located on the ground floor of Padre Rubio Hall, Avenida del Valle, 28 (mmaruri@slu.edu; 915-54-5858, ext. 213) and share the basic fact of your experience with her. The Title IX deputy coordinator will then be available to assist you in understanding all of your options and in connecting you with all possible resources on and off campus.

If you wish to speak with a confidential source, you may contact the counselors at the SLU-Madrid's Counseling Services on the third floor of San Ignacio Hall (counselingcenter-madrid@slu.edu; 915-54-5858, ext. 230) or Sinews Multiplettherapy Institute, the off-campus provider of counseling services for SLU-Madrid (www.sinews.es; 917-00-1979). To view SLU-Madrid's sexual misconduct policy and for resources, please visit the following web address: <http://www.slu.edu/Documents/Madrid/campus-life/SLUMadridSexualMisconductPolicy.pdf>.

Students with Special Needs: In recognition that people learn in a variety of ways and that learning is influenced by multiple factors (e.g., prior experience, study skills, learning disability), resources to support student success are available on campus. Students who think they might benefit from these resources can find out more about:

- Course-level support (e.g., faculty member, departmental resources, etc.) by asking your course instructor.
- University-level support (e.g., tutoring/writing services, Disability Services) by visiting the Academic Dean's Office (San Ignacio Hall) or by going to <https://www.slu.edu/madrid/academics/student-resources>.

Students with a documented disability who wish to request academic accommodations must contact Disability Services to discuss accommodation requests and eligibility requirements. Once successfully registered, the student also must notify the course instructor that they wish to access accommodations in the course. Please contact Disability Services at disabilityservices-madrid@slu.edu or +915 54 58 58, ext. 230 for an appointment. Confidentiality will be observed in all inquiries. Once approved, information about the student's eligibility for academic accommodations will be shared with course instructors via email from Disability Services. For more information about academic accommodations, see "Student Resources" on the SLU-Madrid webpage.

Note: Students who do not have a documented disability but who think they may have one are encouraged to contact Disability Services.

(IF APPLICABLE): Mandatory Trips/Activities: Students enrolled in this class must participate and make payment for all mandatory trips/activities. The prices posted on the web are approximate; the final price will be based on the number of students enrolled on the last day of the Add/Drop period. All students, including those who withdraw from the class after this date, are required to pay these fees, which are non-refundable, unless the trip is cancelled due to low enrollment. Please review SLU-Madrid's trip policies, available on-line.

Course Outline:

Week	Topic
1	Lab 1: AND, OR gates with discrete logic
2	Lab 2: AND, OR gates with FPGA
3	Lab 3: XOR and XNOR gates
4	Lab 4: Parity Generation and Checking
5	Lab 5: Binary Adders
6	Lab 6: Decoders, Encoders and Applications

7	Lab 7: Multiplexers and Demultiplexers
8	Lab 8: Shift Registers
9	Lab 9: Synchronous Counters
10	Lab 10: Simulation
11	Final Project
12	Final Project
13	Final Project
14	Final Project Presentation

Spring 2018 Course Schedule:

JANUARY	
Wednesday 10	First Day of Classes
Thursday 11	
Friday 12	
Monday 15	
Tuesday 16	Lab 1: AND, OR gates with discrete logic
Wednesday 17	
Thursday 18	
Friday 19	
Sunday 21	Last Day to Drop a Class without a Grade W and/or Add a Class; Last Day to Choose Audit (AU) or Pass/No Pass (P/NP) Options
Monday 22	
Tuesday 23	Lab 2: AND, OR gates with FPGA
Wednesday 24	
Thursday 25	
Friday 26	No Classes Application Deadline for Spring Semester Degree Candidates
Monday 29	
Tuesday 30	Lab 3: XOR and XNOR gates
Wednesday 31	
FEBRUARY	
Thursday 1	
Friday 2	
Monday 5	
Tuesday 6	Lab 4: Parity Generation and Checking
Wednesday 7	
Thursday 8	
Friday 9	
Monday 12	
Tuesday 13	Lab 5: Binary Adders
Wednesday 14	Ash Wednesday Registration for Summer 2018 Begins
Thursday 15	
Friday 16	
Monday 19	
Tuesday 20	Lab 6: Decoders, Encoders and Applications
Wednesday 21	
Thursday 22	No Classes (Winter Break)
Friday 23	
Monday 26	
Tuesday 27	Lab 7: Multiplexers and Demultiplexers Professors' Deadline to Submit Midterm Grades

Wednesday 28	
MARCH	
Thursday 1	
Friday 2	
Monday 5	
Tuesday 6	Lab 8: Shift Registers
Wednesday 7	
Thursday 8	
Friday 9	Last Day to Drop a Class and Receive a Grade of W
Monday 12	
Tuesday 13	Lab 9: Synchronous Counters
Wednesday 14	
Thursday 15	Last Day to Submit Transfer Application for Fall Semester
Friday 16	
Monday 19	
Tuesday 20	Lab 10: Simulation
Wednesday 21	
Thursday 22	
Friday 23	
Monday 26	<i>Semana Santa</i> Holiday (Campus Closed)
Tuesday 27	
Wednesday 28	
Thursday 29	<i>Jueves Santo</i> (Campus Closed)
Friday 30	<i>Viernes Santo</i> (Campus Closed)
APRIL	
Monday 2	
Tuesday 3	Final Project
Wednesday 4	Registration for Fall 2018 Semester Begins
Thursday 5	
Friday 6	
Monday 9	
Tuesday 10	Final Project
Wednesday 11	
Thursday 12	
Friday 13	
Monday 16	
Tuesday 17	Final Project
Wednesday 18	
Thursday 19	
Friday 20	
Monday 23	
Tuesday 24	Final Project Presentation
Wednesday 25	
Thursday 26	
Friday 27	
Monday 30	
MAY	
Tuesday 1	<i>Día del Trabajador</i> (Campus Closed)
Wednesday 2	<i>Día de la Comunidad</i> (Campus Closed)
Thursday 3	Spring 2018 Final Day of Classes
Friday 4	Spring 2018 Final Exams
Monday 7	
Tuesday 8	

Wednesday 9	
Thursday 10	
Friday 11	University Housing Move-out Date
Saturday 12	Commencement
Sunday 13	Professors' deadline to submit spring 2018 final grades

Final Exam Schedules Spring 2018

	4 May (Fr)	7 May (Mn)	8 May (Tu)	9 May (Wd)	10 May (Th)
08:30-11:30	Mn classes that meet at 9:00 & 9:30	Mn classes that meet at 10:00	Mn classes that meet at 11:00 & 11:30	Tu classes that meet at 9:30	Tu classes that meet at 8:00
12:00-15:00	Tu classes that meet at 11:00	Mn classes that meet at 13:00	Tu classes that meet at 14:30	Mn classes that meet at 12:00 & 12:30	Tu classes that meet at 12:30
15:30-18:30	Mn classes that meet at 14:30	Tu classes that meet at 17:00 & 17:30	Mn classes that meet at 16:00	Tu classes that meet at 16:00	Mn classes that meet at 17:30
19:00-22:00	---	---	Mn classes that meet at 18:30 & 19:00	Tu classes that meet at 19:00	---